09/945,500 PATENT
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Leonard Forbes

Examiner: Ly Pham

Applicant: Serial No.:

09/945,500

Group Art Unit: 2818

Filed:

August 30, 2001

Docket: 1303.029US

Title:

PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH

LOW TUNNEL BARRIER INTERPOLY INSULATORS

RECEIVED

TERMINAL DISCLAIMER

APR 0 3 2003

Commissioner for Patents Washington, D.C. 20231

OFFICE OF THE SPECIAL PROGRAMS EXAMINER

I, Timothy B. Clise, am an attorney of record for the above identified patent application as evidenced by the Power of Attorney filed in the present application on August 30, 2001. I am making this petition on behalf of Micron Technology, Inc., the assignee of the present invention. As an attorney of record, I am empowered to act on behalf of the assignee and, in accordance with 37 C.F.R. § 1.321(b)(iv), to sign this terminal disclaimer.

Certificate Under 37 C.F.R. §3.73(b)

Your petitioner, Micron Technology, Inc., certifies that it is the owner of the entire right, title and interest in and to the above-identified patent application (Serial No. 09/945,500) and to U.S. Serial No. 09/945,498. Your petitioner owns the entire right, title, and interest of these applications by nature of the assignments executed and filed for both of these applications. The assignment for U.S. Serial No. 09/945,498 was recorded on August 30, 2001 on Reel 012155, Frame 0633, with the United States Patent and Trademark Office. The assignment for U.S. Serial No. 09/945,500 was recorded on August 30, 2001 on Reel 012155, Frame 0529, with the United States Patent and Trademark Office.

The undersigned representative of the assignee has reviewed the evidentiary documents of title and certifies that to the best of assignee's knowledge and belief, title is in the assignee,

Micron Technology, Inc., seeking to take the action set forth in this disclaimer.

03/28/2003 MBLANCO 00000011 09945500

01 FC:1814

110.00 OP-/

TERMINAL DISCLAIMER
APPROVED

APR 0 3 2003

TECHNOLOGY CENTER 2800 SPECIAL PROGRAM CENTER TERMINAL DISCLAIMER

Serial Number: 09/945,500

Filing Date: August 30, 2001
Title: PROGRAMMABI

PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH LOW TUNNEL BARRIER INTERPOLY

INSULATORS

Terminal Disclaimer

Your petitioner hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on U.S. Serial No. 09/945,500, which would extend beyond the expiration date of the full statutory term, as presently shortened by any terminal disclaimers, of any patent which may issue from U.S. Serial No. 09/945,498. Petitioner hereby agrees that any patent so granted on U.S. Serial No. 09/945,500 shall be enforceable only for and during such period that the legal title to any patent which may issue from U.S. Serial No. 09/945,498 and the legal title of U.S. Serial No. 09/945,500 and any patent granted thereon remain common, this agreement to run with any patent granted on U.S. Serial No. 09/945,500 and to be binding upon the grantee, its successors and assigns.

Limitations on the Disclaimer

Your petitioner does not disclaim any terminal part of any patent granted on the above-identified application prior to the expiration date of the full statutory term as presently shortened by any terminal disclaimer of any patent which may be granted on U.S. Serial No. 09/945,498 in the event that it later expires for failure to pay a maintenance fee, is held unenforceable, is found invalid, is statutorily disclaimed in whole or terminally disclaimed under 37 C.F.R. § 1.321(a), has all claims canceled by a reexamination certificate, or is otherwise terminated prior to the expiration date of its statutory term as presently shortened by any terminal disclaimer, except for the separation of legal title as stated hereinabove. Your petitioner does not disclaim any terminal part of any patent granted on the above-identified application if no patent issues from U.S. Serial No. 09/945,498.

TERMINAL DISCLAIMER
APPROVED

Page 2

Dkt: 1303.029US1

APR 0 3 2003

TECHNOLOGY CENTER 2800 SPECIAL PROGRAM CENTER TERMINAL DISCLAIMER

Serial Number: 09/945,500 Filing Date: August 30, 2001

Title: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH LOW TUNNEL BARRIER INTERPOLY

INSULATORS

Fee Status

A check in the amount of \$110.00, which is required under 37 C.F.R. §1.20(d) to file a statutory disclaimer, is enclosed herewith. The Commissioner of Patents and Trademarks is hereby authorized to charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

LEONARD FORBES

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Page 3

Dkt: 1303.029US1

P.O. Box 2938

Minneapolis, MN 55402

(612) 349-9587

Date 24 March 2007

Timothy B. Clise

Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this 24th day of March, 2003.

Name

Signature

TERMINAL DISCLAIMER

APR 0 3 2003

TECHNOLOGY CENTER 2800 SPECIAL PROGRAM CENTER

LISSI MOJICA MARQUIS SPECIAL PROGRAM EXAMINER TECHNOLOGY CENTER 2800